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(54) Electric power transmission network state estimation

(57) In a method, computer program and system for estimating a state of an electric power transmission network, time-stamped phasor measurements are made by phasor measurement units (1) located at a first set of locations (11,12) throughout the network, and a plurality of voltage and/or current phasors at a second set of locations is estimated, where the locations of the first (11,12) and second sets of locations are distant from one another, and a status of network elements

(L4,L5,13) that are remotely located from the phasor measurement units is inferred from the phasor measurements and the estimated phasors.

In a preferred variant of the invention, the network elements are transmission lines (L1...L8), switchgear, under load tap changing transformers, compensation devices (13) or FACTS devices, and the status information describes whether a network element (L4,L5,13) is electrically connected to or disconnected from the network.

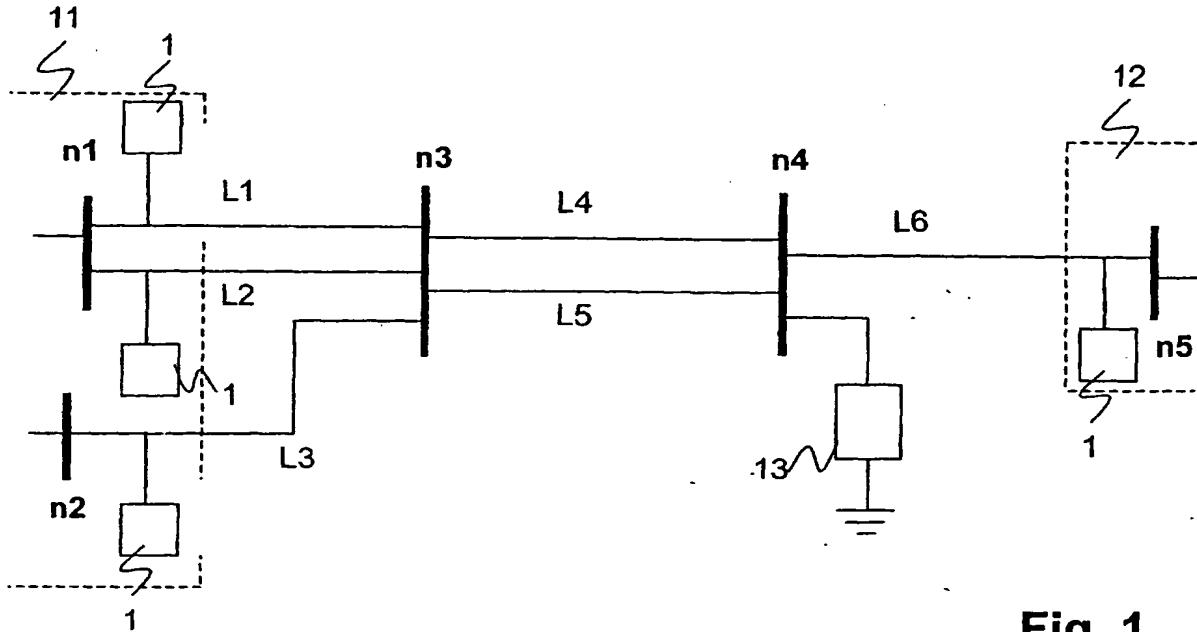


Fig. 1

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Description

Field of the Invention

[0001] The invention relates to large-scale electric power transmission networks, and, more particularly, to a method, a computer program and a system for estimating a state of an electric power transmission network according to the preamble of claims 1, 8 and 9, respectively.

Background of the Invention

[0002] Electric power transmission and distribution systems or networks comprise high-voltage tie lines for connecting geographically separated regions, and substations for transforming voltages and for switching connections between lines. Power generation and load flow in a network with several substations is controlled by an energy management system. For managing the network, it is desirable to determine a state of the network, in particular load flows and stability margins. Measurements of average RMS (root mean square) values of voltages, currents, active power and reactive power flowing in the network and/or measurements of voltage and current phasors are made at various places in the network and collected by substation automation (SA) systems and by a system control and data acquisition (SCADA) system. The SA and SCADA systems further provide information about a status of network elements such as switchgear, compensation devices and variable transformers. The status of switchgear (i.e. circuit breakers, disconnectors, etc.) allows to infer the topology of the network, i.e. how the lines and devices are connected, and which lines, power generators and consumers are disconnected. Additionally, measurements of voltage and current phasors are made and collected. All the abovementioned measurements from throughout the network are collected at the energy management system, providing a snapshot of the state of the network. The snapshot allows to compute the state of the network, as described e.g. in "State estimation with phasor measurements", Phadke et. al., IEEE Transactions on power systems, Vol. PWRS-1, No. 1, Feb. 1996. There, all positive sequence bus voltages and some positive sequence currents are measured, the network topology is assumed to be known, and unknown voltages, phase angles and power flows are computed.

Description of the Invention

[0003] It is an object of the invention to create a method, a computer program and a system for estimating a state of an electric power transmission network of the type mentioned initially, which uses less measurements.

[0004] These objects are achieved by a method, a computer program and a system for estimating a state of an electric power transmission network according to

the claims 1, 8 and 9, respectively.

[0005] In the inventive method, computer program and system for estimating a state of an electric power transmission network, time-stamped phasor measurements are made by phasor measurement units located at a first set of locations throughout the network, and a plurality of voltage and/or current phasors at a second set of locations is estimated, wherein the locations of the first and second sets of locations are distant from one another, and a status of network elements that are remotely located from the phasor measurement units is inferred from the phasor measurements and from the estimated phasors.

[0006] This allows to estimate a network state without having to collect status information of network elements such as switchgear, compensation and FACTS devices and variable transformers through a SCADA or a Substation Automation system.

[0007] An additional advantage is conferred by the fact that phasor measurements incorporate high resolution timing information. It therefore is also possible to obtain a much better temporal resolution for the state estimation, since conventional SCADA systems do not provide time stamped information about e.g. switchgear status. This better temporal resolution allows faster and better quality control of the network.

[0008] In a preferred variant of the invention, the network elements are transmission lines, switchgear, under load tap changing transformers, compensation devices or FACTS devices, and the status information describes whether a network element is electrically connected to or disconnected from the network and optionally also describes element specific parameters such as a transformer tapping position or a FACTS impedance value.

[0009] For switchgear such as breakers or disconnectors, the status information describes whether a switch is open or closed.

[0010] In a further preferred variant of the invention, the state that is estimated comprises current and load flows, and optionally indicators characterising network stability. This is possible, since the invention allows to determine, from phasor measurements alone, which network elements are connected, i.e. the network topology. No separate measurements or status information about the network topology are required. Therefore, the invention can work completely independent of and redundant to existing SCADA systems.

[0011] The computer program for estimating a state

of an electric power transmission network according to the invention is loadable into an internal memory of a digital computer, and comprises computer program code means to make, when said computer program code means is loaded in the computer, the computer execute the method according to the invention. In a preferred embodiment of the invention, a computer program product comprises a computer readable medium, having the computer program code means recorded thereon.

[0012] Further preferred embodiments are evident from the dependent patent claims.

Brief Description of the Drawings

[0013] The subject matter of the invention will be explained in more detail in the following text with reference to preferred exemplary embodiments which are illustrated in the attached drawings, in which:

- Figure 1 schematically shows part of an electric power system,
- Figure 2 shows an equivalent circuit for a transmission line
- Figure 3 schematically shows an electrical substation
- Figure 4 shows an equivalent circuit for a tap changing transformer
- Figure 5 schematically shows the structure of a system according to the invention.

[0014] The reference symbols used in the drawings, and their meanings, are listed in summary form in the list of reference symbols. In principle, identical parts are provided with the same reference symbols in the figures.

Detailed Description of Preferred Embodiments

[0015] Figure 1 schematically shows part of an high- or medium voltage electric power system or network comprising nodes n1 ... n5 such as busbars, and transmission lines L1 ... L6. A plurality of nodes n1, n2 is located at a first substation 11, another node n5 is located at a second substation 12. A compensation element 13 is connected to a fourth node n4. At the first and second substation 11, 12, phasor measurement units (PMU) 1 are configured to measure voltage and/or current phasors at the lines L1, L2, L3, L6 leaving the respective substation 11, 12. A third node n3 and the fourth node n4 are not equipped with PMU's. The third node n3 and fourth node n4 are located remote or geographically distant from one another and from the first substation 11 and the second substation 12. They are separated by the lengths of the lines L1...L6 connecting them, typically at distances between one kilometre and several tens or hundreds of kilometres. Where the lines meet the nodes, switchgear devices, not shown in the figures, are configured for electrically disconnecting lines.

[0016] Phasor data is determined with a phasor measurement unit residing, for example, at a feeder at the bay level of substations or at branching points along transmission lines. A voltage phasor represents, for example, a voltage of the feeder or line, while a current phasor represents current flowing through the feeder or line.

[0017] The phasor data represents a phasor and may be a polar number, the absolute value of which corresponds to either the real magnitude or the RMS value of a quantity, and the phase argument to the phase angle at zero time. Alternatively, the phasor may be a complex number having real and imaginary parts or the phasor may use rectangular or exponential notation. By contrast, conventional sensing devices used in power networks generally measure only scalar, average representations, such as the RMS value of a voltage, current etc.

[0018] The phasor data is collected from phasor measurement units that are distributed over a large geographic area, i.e. over tens to hundreds of kilometres. Since the phasor data from these disparate sources are analysed in conjunction, they must refer to a common phase reference. Therefore, the different phasor measurement units must have local clocks that are synchronised to within a given precision. Such a synchronisation of the phasor measurement units is preferably achieved with a known time distribution system, for example the global positioning (GPS) system. In a typical implementation, the phasor data 9 is determined at least every 200 or every 100 or preferably every 40 milliseconds, with a temporal resolution of preferably less than 1 millisecond. In a preferred embodiment of the invention, the temporal resolution is less than 10 microseconds, which corresponds to a phase error of 0.2 degrees. Each measurement is associated with a time stamp derived from the synchronised local clock. The phasor data therefore comprises time stamp data.

[0019] According to the invention, it is possible to determine a status of the fourth line L4 and the fifth line L5, i.e. whether said lines are connected or disconnected, without measurements or SCADA data about a status of switchgear at the third node n3 or the fourth node fourth node n4, and even without phasor measurements made at the third or fourth node n3, n4. The term "status" is used to denote properties of network elements such as "connected/disconnected", "open/closed", "tap ratio value" that usually are set by a control device and an actuator associated with a network element,

[0020] The term "state" comprises both the "status" of network elements as well as e.g. current and power flows throughout the network, and optionally closeness to instability. The state is a result of electrical parameters of the network and of network topology, which is determined by the status of network elements.

[0021] Figure 2 shows a standard π -equivalent circuit for a transmission line or line segment. V1 is a voltage phasor at the first node n1, I11 is a phasor of a current

flowing out of the first node n1 into the first line L1. Both V1 and I11 are measured by a PMU 1 located at the first node n1. The first line L1 is represented by a line admittance Y1 and shunt admittances Y11 and Y31, with Y11=Y31. By convention, for a current, the first index denotes the node from which the current flows, and the second index denotes the line into which it flows. For a shunt admittance, the first index denotes the node at the side of the line where the shunt lies, and the second index denotes the line.

[0022] V3 is an unknown voltage phasor at the third node n3. V3 is calculated as

$$V3 = \frac{(Y1+Y11)V1-I11}{Y1} \quad 15$$

[0023] In a similar fashion, referring to the power system shown in Fig. 1, the voltage V4 at the fourth node n4 is calculated as

$$V4 = \frac{(Y6+Y56)V5-I56}{Y6} \quad 20$$

from the voltage and current phasors V5,I56 measured at the fifth node n5.

[0024] The currents flowing through the first, second and third line L1,L2,L3 into the third node n3 are calculated from V3 and the measurements. For example,

$$I31 = (Y1+Y31)V3 - Y1 V1$$

[0025] The total current from the third node n3 to the fourth node n4 therefore is

$$I_{3to4} = - (I31 + I32 + I33). \quad 35$$

If this current is zero, then the status of both the fourth and fifth line L4,L5 is "disconnected". If it is nonzero, it is necessary to determine which of said lines L4,L5, if any, is disconnected. Assuming that only the fourth line L4 is connected and that the fifth line L5 is out of service, the current flowing into the fourth line L4 from the third node n3 can be calculated as

$$I34 = (Y4 + Y34)V3 - Y4 V4$$

which ought to be equal to I_{3to4} . If it is equal, then the assumption is valid. If it is not equal, then the inverse assumption "only the fifth line L5 is connected" is made and checked. If this assumption is invalid as well, then both lines L4,L5 are connected.

[0026] In the unlikely case that the admittances of the fourth line L4 and the fifth line L5 are be equal, it is only possible to determine whether none, one or both of these lines L4,L5 is connected. It is not possible to de-

termine which of the two is disconnected. This is, however, not relevant for a load flow calculation that uses this topology information - exactly because the line admittances are equal, regardless of which of the two lines L4,L5 is disconnected.

[0027] In order to determine a status of the compensation element 13 that is not observed directly, i.e. it is not equipped with a PMU 1, the procedure for determining voltages and detecting line status as shown above is executed. Then, all currents flowing into the fourth node n4 are computed, depending on the network topology. For example, if both the fourth and fifth line L4,L5 are connected, and from

$$I45 = (Y5 + Y45)V4 - Y5 V5 \quad 15$$

it follows that the sum of currents flowing into the fourth node n4 through lines L4,L5 and L6 is

$$I_{4in} = - (I45 + I44 + I46). \quad 20$$

If I_{4in} is zero, then the compensation element 13 is disconnected. If I_{4in} is not zero, the compensation element 13 is in use.

[0028] For FACTS (flexible AC transmission) devices, with knowledge about the internal workings of the device, status information may be determined from the current and voltage phasors at the device. Regardless of the internal workings, whether a FACTS device is connected or not is inferred in the same way as shown above for the compensation element 13.

[0029] Figure 3 schematically shows an third electrical substation 13 with busbars acting as nodes n6,n7, a transformer 31 connected to a sixth node n6, a busbar connector 32 connecting the sixth node n6 to a seventh node n7, and lines L7,L8 and a load 33, each line and load 33 being connected to one of the nodes n6,n7 and having an associated PMU 1. Although the busbar connector 32 is located in the third substation 13, no direct observation with a PMU 1 or current transducer is available. Therefore, the status of the busbar connector 32 is inferred from a currents balance, using phasor measurements at either the sixth node n6 or the seventh node n7. For example, if

$$I_{6to6} = - (I_{transformer} + I67) \quad 40$$

is zero, then the busbar connector 32 is open. Otherwise, it is closed.

[0030] Transformers without automatic tap changing are modelled like static line elements. i.e. by symmetrical π -equivalent circuits, and have only an on/off status.

[0031] Figure 4 shows an equivalent circuit for an under load tap changing (ULTC) transformer arranged between nodes with voltage phasors V1 and V2 and a cur-

rent phasor I_2 flowing into the transformer from the node with voltage V_2 . The admittances of the circuit depend on a tap ratio t . The ULTC transformer is equipped with a control device that adjusts the voltage ratio of the transformer within certain limits around a nominal voltage ratio. The control device tries to drive a regulated voltage, in this case V_2 , at one side of the transformer to within a deadband around a given nominal value V_{2nom} . This adjustment is represented by the tap ratio t . For $t=1$, the transformer is set to the nominal voltage ratio. Typically, the tap and voltage ratio can be varied by 5% or 10% around the nominal value. In order to determine the current and load flows of the power network, the tap ratio t must be known. According to the invention, it is not necessary to obtain information from the local ULTC transformer's control device. Instead, the tap ratio t is deduced from phasor information. The phasor information is determined either by one or more PMUs 1 located close to the ULTC transformer, or is inferred from remotely located PMUs 1 as described above. Likewise, it is determined whether the ULTC transformer is connected at all. Parameters of the ULTC transformer that must be known are its nominal admittance Y , incremental tap ratio, minimal and maximal tap ratio t and a deadband area around the nominal regulated value.

[0032] Two cases are possible: Either the actual or present value V_{2nom} of the regulated voltage is known, or it is not known.

[0033] For the first case, the tap ratio t is determined from the regulated voltage V_2 alone. Let $|I|$ denote the absolute value of a complex variable. Then,

$$\text{if } |V_2| < 0.9|V_{2nom}| \Rightarrow t = 0.9$$

$$\text{if } |V_2| > 1.1|V_{2nom}| \Rightarrow t = 1.1$$

$$\text{if } 0.98 < \frac{|V_2|}{|V_{2nom}|} < 1.05 \Rightarrow t = 1$$

where 0.98 and PMU 1.05 are bounds of the deadband, and in all other cases, the tap ratio t is rounded to the nearest possible value, according to the incremental tap ratio.

[0034] The admittances of the equivalent circuit are calculated from their nominal values and from the tap ratio t as

$$Y_{ULTC} = t Y$$

$$Y_{1ULTC} = t(t-1) Y$$

$$Y_{2ULTC} = (1-t) Y$$

[0035] For the second case, when the nominal value V_{2nom} of the regulated voltage is not known, the voltage phasors V_1, V_2 on both sides of the ULTC transformer, and the current phasor I_2 on the side of the regulated voltage V_2 must be known. The tap ratio t is calculated as

$$t = \frac{Y V_2 - I_2}{Y V_1} .$$

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[0036] For a given network topology, the placement of PMUs 1 and the exact sequence of operations for computing voltages and inferring the status of network element status is determined e.g. manually and with

15 computer assistance in an engineering phase of the system according to the invention. Preferably, a set of PMUs 1 that is required for other purposes is given. Starting with their measurements, equations for determining as many voltages and current phasors at nodes and lines not equipped with PMUs are generated. This is done for all possible topologies, i.e. for all network elements being either connected or disconnected. If, for certain cases, the existing phasor measurements are not sufficient, an additional PMU 1 is introduced either 20 at the location where information is required, or at a node connected to said location.

[0037] In a preferred embodiment of the invention, the placement of PMUs 1 is minimised in a two-step procedure: in a first step, the minimum number of substations 30 equipped with PMUs 1 is determined, assuming that in such a substation, each node equipped with an instrument transformer has an associated PMU. In a second step, a minimum number of PMU's for each substation is determined.

35 [0038] Figure 5 schematically shows the structure of a system according to the invention. Each of a plurality of PMUs 1 transmits current and/or voltage phasor information to a network element status detection 2. The network element status detection 2 is a data processing device configured to determine the status of network elements, as shown above. It is configured to receive, as an additional input, network element parameters 3 such as line and shunt admittances, tap ratio increments, limits and deadband values, regulated voltages etc. In a preferred embodiment of the invention the network element status detection 2 transmits the status of network elements together with the phasor information as status and phasor information 4 to a state calculation/estimation data processing device 5. This information 4 is used

40 by the state calculation/estimation 5 to estimate a state of the network, e.g. by doing a load flow analysis, in which topology information is given by the status of network elements without the need of additional measurements.

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List of designations

[0039]

1	phasor measurement unit (PMU)	5
2	network element status detection	
3	network element parameters	
4	status and phasor information	
5	state calculation/estimation	
11	first substation	10
12	second substation	
13	compensation element	
13	third substation	
31	transformer	15
32	busbar connector	
33	load	
n1, n2...n7	first node, second node, ... seventh node	
L1, L2, ... L8	first line, second line, ... eighth line	20
I ..	current phasor	
V ..	voltage phasor	
Y ..	complex admittance	
t	tap ratio	25

Claims

1. A method for estimating a state of an electric power transmission network, in which time-stamped phasor measurements are made by phasor measurement units (1) located at a first set of locations (11,12) throughout the network, **characterized in that** a plurality of voltage and/or current phasors at a second set of locations is estimated, and wherein the locations of the first (11,12) and second sets of locations are distant from one another, and that a status of network elements (L4,L5,13) that are remotely located from the phasor measurement units (1) is inferred from the phasor measurements and the estimated phasors. 30
2. The method according to claim 1, in which voltages and/or currents at a substation that is not equipped with phasor measurement units (1) is estimated. 45
3. The method according to one of the preceding claims, in which a state of the network comprising load and current flows of the network is estimated from the measured and estimated voltages and currents and from an estimated topology of the network. 50
4. The method according to one of the preceding claims, in which the network elements comprise switchgear and the status of network elements comprises information on whether a switch is open or closed. 55
5. The method according to one of the preceding claims, in which the network elements comprise transmission lines (L1...L8) and the status of network elements comprises information on whether a transmission line is disconnected or in service.
6. The method according to one of the preceding claims, in which the network elements comprise under load tap changing transformers and the status of network elements comprises information on a tap ratio of the transformer.
7. The method according to one of the preceding claims, in which the network elements comprise at least one compensation device (13) or FACTS device and the status of network elements comprises information on whether said device is electrically connected to or disconnected from the network.
8. A computer program for estimating a state of an electric power transmission network loadable into an internal memory of a digital computer, comprising computer program code means to make, when said program is loaded in the computer, the computer execute a procedure for processing time-stamped phasor measurements originating from phasor measurement units (1) located at a first set of locations (11,12) throughout the network, **characterized in that** the program is configured to compute a plurality of estimated voltage and/or current phasors at a second set of locations, the locations of the first (11,12) and second sets of locations being distant from one another, and to compute, from the phasor measurements and from the estimated phasors, a status of network elements (L4,L5,13) that are remotely located from the phasor measurement units. 35
9. A system for estimating a state of an electric power transmission network from time-stamped phasor measurements that originate from phasor measurement units (1) located at a first set of locations (11,12) throughout the network, **characterized in that** the system comprises a data processing device configured to compute a plurality of estimated voltage and/or current phasors at a second set of locations, the locations of the first (11,12) and second sets of locations being distant from one another, and to compute, from the phasor measurements and from the estimated phasors, a status of network elements (L4,L5,13) that are remotely located from the phasor measurement units. 40
10. The system according to claim 9, wherein the data processing device is configured to estimate voltages and/or currents at a substation that is not

equipped with phasor measurement units (1).

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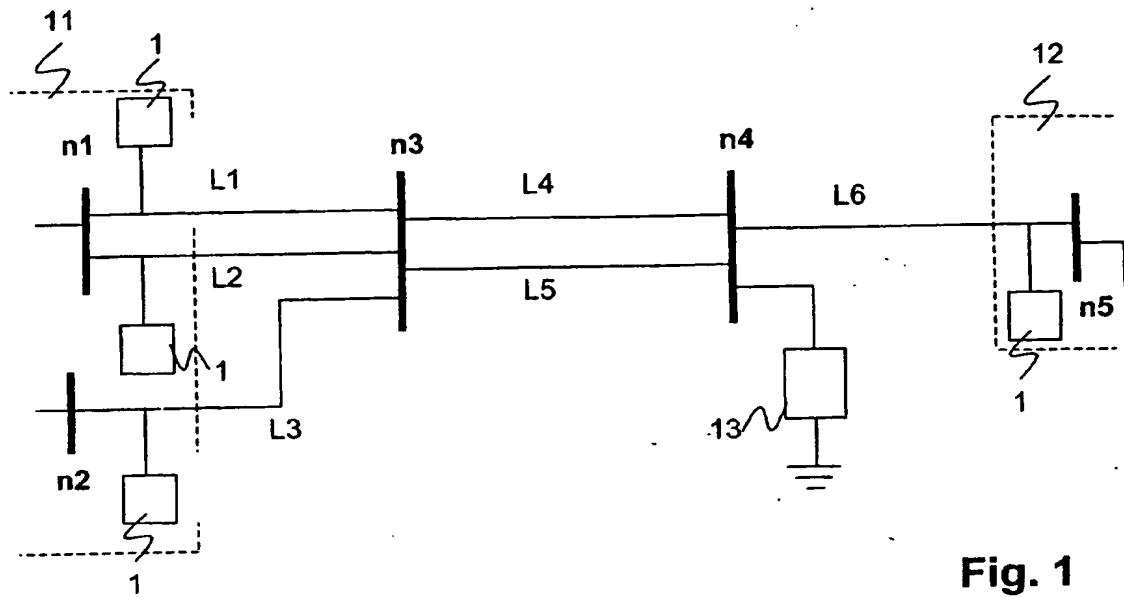


Fig. 1

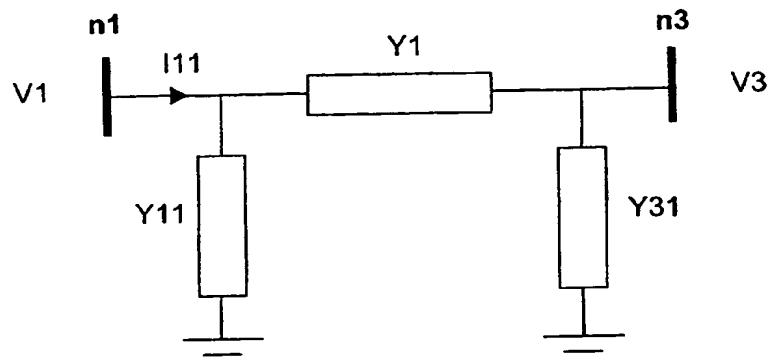


Fig. 2

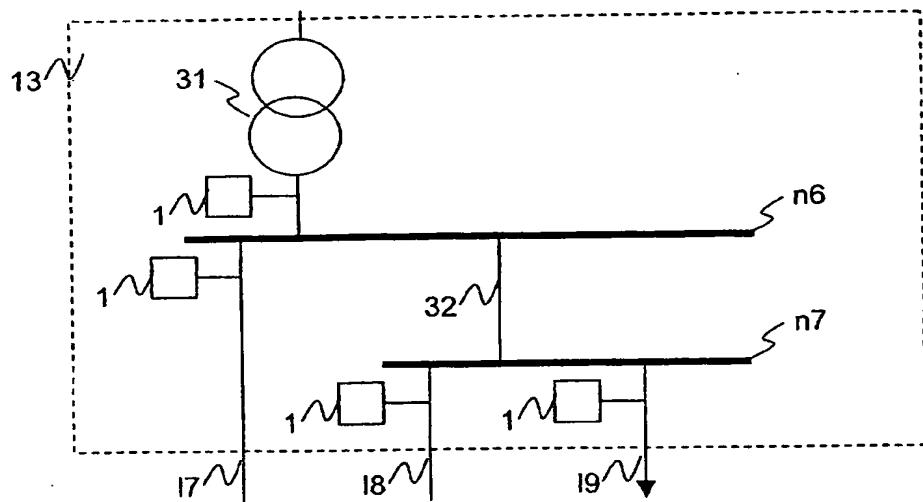


Fig. 3

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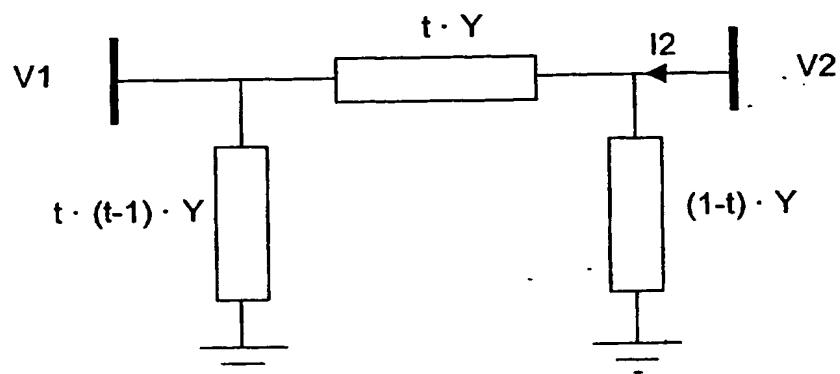


Fig. 4

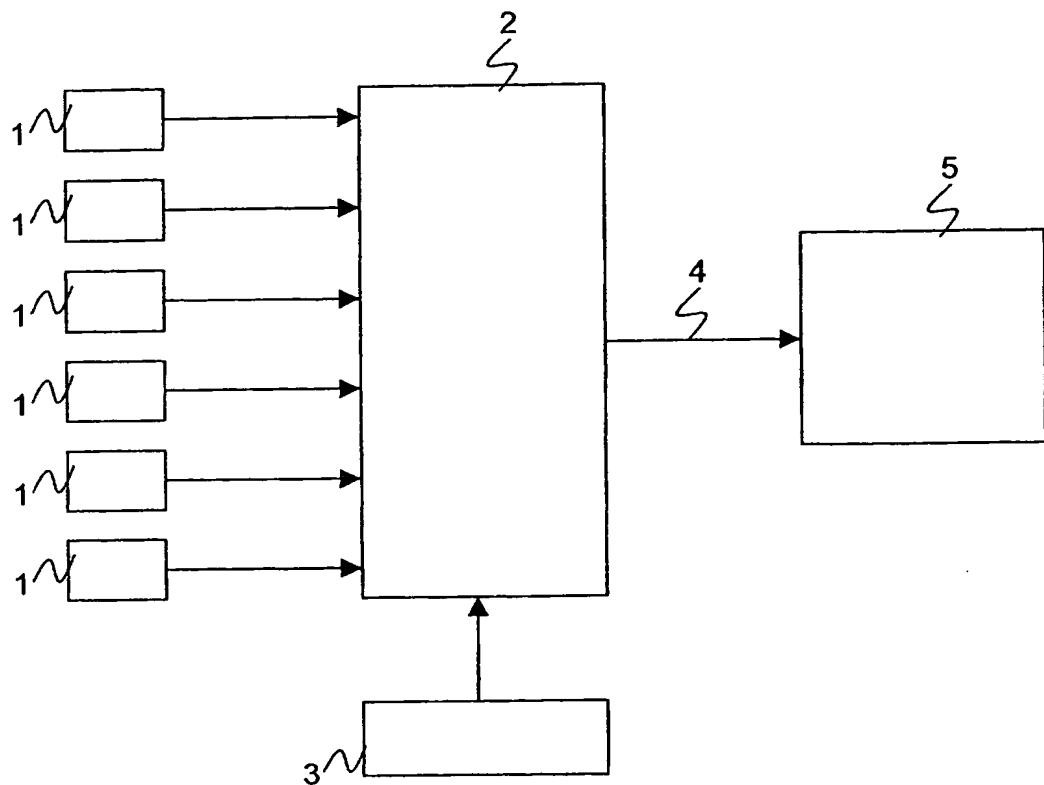


Fig. 5

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DOCUMENTS CONSIDERED TO BE RELEVANT			CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	
Y	EP 0 795 944 A (AEG ATLAS SCHUTZ UND LEITTECHN) 17 September 1997 (1997-09-17) * page 15, line 48; figure 5 * * page 7, line 7-12 * * page 7, line 18-21; figure 1 * * page 1, line 5,6 * * page 17, line 7-26 * * page 12, line 19-25 * * page 17, line 3-10 * * page 13, line 42-50 * * page 20, line 52,53; figure 1 * * page 7, line 45-55 * * page 1, line 9-11; figure 2 * * page 3, line 3 * * page 20, line 37 * * page 7, line 10; figure 1 * * page 12, line 30,31; figure 1 * * page 3, line 10,11 * * page 1, line 8-11 * -----	1 2 3 4-7 8 9 10	H02H7/26 H02H3/40 H02J3/24
Y	EP 1 134 867 A (ABB POWER AUTOMATION AG) 19 September 2001 (2001-09-19) * column 9, line 28; figure 2 * * column 8, line 27-31; figure 2 * * column 8, line 36-40 * * column 2, line 3 * * column 10, line 3 * * column 4, line 10-13 * * column 9, line 26,27; figure 2 * * column 10, line 44-52; figure 2 * * column 5, line 18-21 * * column 35-36 * * column 10, line 55-58 * -----	1 2 3 4-7 8 9 10	TECHNICAL FIELDS SEARCHED (Int.Cl.7) H02H H02J
The present search report has been drawn up for all claims			
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MUNICH	24 June 2002	Kanelis, K	
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EP 01 81 1253

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Patent document cited in search report		Publication date		Patent family member(s)		Publication date
EP 0795944	A	17-09-1997	DE EP	19609595 A1 0795944 A2		23-10-1997 17-09-1997
EP 1134867	A	19-09-2001	EP JP US	1134867 A1 2001268794 A 2001021896 A1		19-09-2001 28-09-2001 13-09-2001

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